



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jung-Sook GOO et al.

Title: STRAINED SILICON MOSFET
HAVING IMPROVED THERMAL
CONDUCTIVITY AND METHOD
FOR ITS FABRICATION

Application No.: 10/658,479

Filing Date: 9 September 2003

Examiner: H. TRINH

Art Unit: 2814

CERTIFICATE OF EXPRESS MAILING
I hereby certify that this correspondence is being deposited with the United States Postal Service's "Express Mail Post Office To Addressee" service under 37 C.F.R. § 1.10 on the date indicated below and is addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

EV 445625798 US

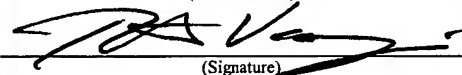
(Express Mail Label Number)

November 9, 2004

(Date of Deposit)

Ruthie Vallejo

(Printed Name)



(Signature)

REPLY TO OFFICIAL ACTION OF 25 AUGUST 2004
UNDER 37 CFR §1.111

Mail Stop AMENDMENT
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby replies to the official action mailed 25 August 2004.

Reconsideration in view of the following amendments and remarks is requested.